## ABSTRACT

A plasma display panel in which a first substrate having a protective layer formed thereon opposes a second substrate across a discharge space, with the substrates being sealed around a perimeter thereof. At a surface of the protective layer, first and second materials of different electron emission properties are exposed to the discharge space, with at least one of the materials existing in a dispersed state. The first and second materials may be first and second crystals, and the second crystal may be dispersed throughout the first crystal.

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